

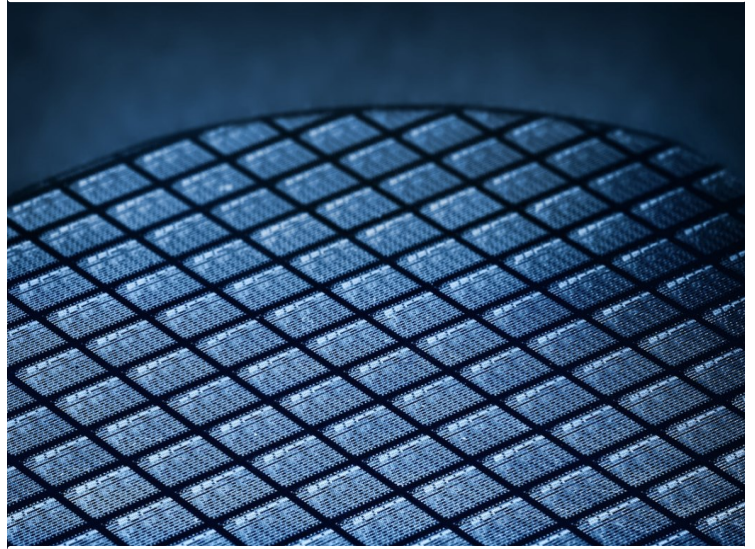
Senior RFIC Design Engineer for Advanced Opto-Electronic modules

Country: Italy

City: Vimercate (Milan)

Job Field Area: R&D

Experience Required: 2-5 years



Nokia is committed to innovation and technology leadership across mobile, fixed and cloud networks. Your career here will have a positive impact on people's lives and will help us build the capabilities needed for a more productive, sustainable, and inclusive world. We challenge ourselves to create an inclusive way of working where we are open to new ideas, empowered to take risks and fearless to bring our authentic selves to work.

The pandemic has highlighted how important telecoms networks are to society. Nokia's Network Infrastructure group is at the heart of a revolution to bring more and faster network capacity to people worldwide through our ambition, innovation, and technical expertise. Nokia Advanced Optics team develops optical front ends for next generation optical coherent systems for metro as well as long-reach subsea communication links. Within our supportive and innovative work environment you will find space for individual thinking and creativity to ensure quality and reliability of our highly integrated products.

Position Description

Are you passionate about anticipating and solving problems? Are you a collaborative team player who can work independently? We are currently looking for analog and mixed-signal IC designers to contribute to the design of high-speed drivers, TIAs and control systems for optical communication applications.

Your main activities will be:

- Lead or support the support architecture definition, design, layout, and qualification of the electronic circuits co-designed with photonic integrated circuits, targeting specific high-speed communication products
- Develop new design approaches and innovate on both device-level design and system-level optimization
- Interact with fab partners
- Contribute to test development and lead the test data analysis of your devices to correlate design with measurements
- Collaborate with all other functional teams in the organization to deliver co-designed and co-optimized photonics systems

Position Requirements

As applicant you should be in possession of M.S. or Ph.D. in Electrical Engineering or equivalent practical work experience:

- Background in high-speed analog and mixed-signal integrated circuit design, with fundamental concepts such as: circuit noise and linearity, linear amplifiers, low noise linear TIAs, AGC circuits, linear equalizers
- Experience in testing and debugging analog and mixed signal high-speed ICs as well as experience with physical design and verification (passive component design using industry-standard tools such as Cadence Virtuoso and HFSS, EMX)
- Very good English skills fluent in spoken and written
- Good communication skills
- Ability to work effectively and respectfully with diverse teams across multiple locations worldwide internally and with external partners

It would be nice if you also had:

- Familiarity with CMOS design and manufacturing; knowledge of SiGe and BiCMOS
- Experience with silicon photonics and/or optical communications
- Publications and a record of innovation
- Familiarity with digital flow (synthesis, static timing, place and route), particularly in the context of state machines for mixed-signal control systems
- Background in wireline communication and equalization; familiarity with SerDes, CDR, PLL, ADC, and/or DAC

Interested? Please apply at careers.nokia.com with job code 2200000343. If you have any questions regarding this position, please contact Paola Galli (paola.galli@nokia.com).

